



FACULTY DEVELOPMENT PROGRAMME (FDP) ON

BIO-INSPIRED ALGORITHMS FOR INTELLIGENT SIGNAL PROCESSING (24th April to 30th April 2019)

Organized by

Vardhaman College of Engineering (Autonomous), Shamshabad, Hyderabad
in association with

E & ICT Academy, National Institute of Technology, Warangal.

(Sponsored by Ministry of Electronics and Information Technology (MeitY), GOI)



Preamble:

"Electronics & ICT Academy" was set up at NIT Warangal with financial assistance from MeitY, GoI. The jurisdiction of this academy is Telangana, Andhra Pradesh, Karnataka, Goa, Puducherry and Andaman & Nicobar Islands. This academy role is to offer faculty development programmes in standardized courses and emerging areas of Electronics, Information Communication Technologies, training & consultancy services for Industry, Curriculum development for Industry, CEP for working professionals, Advice and support for technical incubation and entrepreneurial activities.

About the Workshop:

As part of an initiative of E&ICT, a Faculty Development Programme (FDP) on "Bio-inspired Algorithms for Intelligent Signal processing" is being conducted at Departments of ECE at Vardhaman College of Engineering (Autonomous) to the faculty members of Engineering, MCA, Research Scholars and other allied disciplines from Telangana, Andhra Pradesh, Karnataka, Pondicherry, Goa, Andaman and Nicobar Islands.

Major Course Contents:

- Intelligent systems design
- Particle swarm Optimization (PSO)
- Differential Evolution (DE)
- Bat Algorithm (BA)
- Teacher Learning based Optimization Algorithm (TLBO)
- Sine Cosine Algorithm (SCA)
- Jaya Algorithm
- Grey Wolf Optimizer (GWO)
- Hybridization Mechanism
- Convolutional Neural Networks

Applications

- Image processing and Computer Vision
- Channel Equalization
- Antenna Design
- Adaptive System Identification
- Wireless Sensor Networks

Faculty conducting this programme:

The programme will be conducted by the faculty members from NIT Warangal; Academicians in the concerned field from IITs/NITs/IIITs are invited to deliver lectures in the programme. Speakers from industries are also expected to deliver as part of the course.

Eligibility:

The programme is open to faculty of Engineering Colleges, MCA Colleges and other allied disciplines in India. Industry personnel working in the concerned /allied discipline can also attend.

Registration Fee Particulars:

Faculty and Research Scholars	Rs. 2500/-
Faculty of SC/ST Category	Rs. 1875/- (SC/ ST participants should submit the copy of their caste certificate to claim the concession along with application form)
Industry Participants	Rs. 7500/-

SC/ST concession is only for faculty of mentioned states. Research Scholars are not eligible for SC/ST concession.

The fee is to be paid either in the form of DDs or online transfer using the following details:

DD Details	Online Transfer Details
Demand Draft in favor of "Director, NIT Warangal" payable at any bank in Warangal	Account Name: Electronics & ICT Academy NITW Account No: 62423775910 IFSC: SBIN0020149

Accommodation:

All the outstation selected participants will be provided boarding and lodging in nearby college hostels with nominal cost.

Venue: Department of ECE, Vardhaman College of Engineering

How to apply:

A filled in form of application in the prescribed format duly signed and sponsored by appropriate authorities (Along with demand draft) should reach the coordinator by speed post. It is also mandatory to send scanned application form and demand draft through email to hathiram.iisc@gmail.com as selection will be intimated only through mail.

Selection Criteria:

Selection will be done based on first-come-first-serve basis to a maximum number of 50 (fifty). Additionally 10 participants from industry are allowed to participate. The list of selected participants will be intimated through e-mail. In case a candidate is not selected, the DD will be sent back. Candidates will be issued satisfactory certificates on successful completion of the course. Reservations are followed for selecting candidates as per GOI norms.

Important dates:

Last date (Application & DD)	16 th April, 2019
Selection List by E- mail	18 th April, 2019
Duration	24 th – 30 th April, 2019

About NIT Warangal:

National Institute of Technology, Warangal is the first among 17 RECs setup as joint venture of the Government of India and the state government. Over the years the college has established itself as a premier Institute imparting technical education of a very high standard leading to the B.Tech degrees in various branches of engineering, M.Tech. and Ph.D programs in various specializations. All B. Tech and M. Tech programmes of NIT Warangal are NBA accredited.

About ECE Department, VMEG:

The Department of Electronics and Communication Engineering has been playing a vital role in producing quality engineers ever since it was established in the year 1999. With its Qualified, Experienced and committed Faculty/Staff, the department has expanded its activities enormously over the past 20 years. The department runs one under graduate program and two post graduate programs to cater to the ever – changing needs of technical excellence in all areas of Electronics and Communication Engineering such as VLSI Design, Embedded Systems, Telecommunications, Signal Processing etc. It has well equipped laboratories, its own computer centers and departmental library to cater the needs of UG, PG students. A Continuous stream of short-term courses, refresher courses, workshops and winter/summer schools is the general outlay of the department.



APPLICATION FORM

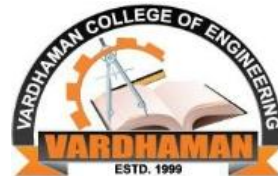
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1. Name :
2. Designation :
3. Institution :
4. Email :
5. Mobile No. :
- 6.

DD	Online Transfer
DD No:	Reference No:
Amount:	
Bank :	
Date:	

7. Address for Correspondence:

8. Educational Qualification:

9. Subjects taught so far:

10. No. of refresher courses/workshops attended:

11. Experience (in years):

Teaching: Research: Industry:

12. Accommodation required: YES /NO

13. Do you belong to SC/ST : YES /NO

(If yes, please specify and attach a copy of caste certificate to claim the concession)

Declaration

The information provided is true to the best of my knowledge. If selected, I agree to abide by the rules and regulations of the FDP and shall attend the course for the entire duration. I also undertake the responsibility to inform the Coordinator in case, I am unable to attend the course.

Signature of the Applicant

SPONSORSHIP CERTIFICATE

Dr. /Mr. /Ms. is an employee of our Institute/Organization and is hereby sponsored to participate in the FDP on "Bio-inspired Algorithms for Intelligent Signal processing", sponsored by Electronics & ICT Academy during 24th April to 30th April 2019 at Vardhaman College of Engineering, Hyderabad.

Signature of Head of Institution
(With seal)

Address for correspondence

Post your application form with DD to

Dr. Hathiram Nenavath,
Associate Professor, Department of ECE,
Vardhaman College of Engineering (Autonomous)
Kacharam, Shamshabad,
Hyderabad, Telangana, India, Pincode-501 218.
Email: hathiram.iisc@gmail.com
Mobile: +91-9000747143

E-mail the scanned copies of filled-in and duly signed application form along with DD to

hathiram.iisc@gmail.com

For more details about Electronics & ICT Academy, NIT, Warangal, please visit:
<https://nitw.ac.in/eict>

Coordinators

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