

Dr. JOSEPH ANTHONY PRATHAP

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Current Research Interests: Currently, Dr. Joseph Anthony Prathap's research is focused on

1. Field Programmable Gate Array based Power Converter Control techniques.
2. FPGA based Machine Learning and Deep Learning algorithm for real applications.

Biographical Information:

Dr. Joseph Anthony Prathap was born in 1981 in Puducherry. He has obtained B.E [Electronics and Communication] and M. Tech [VLSI Design] degrees in 2003 and 2007 respectively, and a Ph.D. in FPGA-based Power Converters in 2017 from Annamalai University. He has put in 15 years of service in teaching and research. He is currently an Associate Professor in the Department of Electronics and Communication Engineering at Vardhaman College of Engineering, Shamshabad, Telangana, India. His research interest includes VLSI design, development of digital switch patterns, FPGA control techniques for power converters, photovoltaic power electronics converters.

Selected Publications: (Top 5 Papers)

1. Joseph Anthony Prathap, T.S.Anandhi "A novel parallel duty cycle control algorithm for photovoltaic voltage regulator system using FPGA" to the Microprocessor and Microsystems, Elsevier, Volume 65, pp: 107-120.[ISSN No: 0141-9331, IF:2.091] [ISI INDEXED, SCI INDEXED, Thomson Reuters "Web of Science", SCOPUS INDEXED].
2. Joseph Anthony Prathap, T.S.Anandhi, Nagarjuna Malladhi, V.Roja "Investigation of FPGA based 32-bit RISC-Modulation Processor" to the International Journal of Computer Information Systems and Industrial Management Systems, Volume 10, pp: 227-237, August 2018. [ISSN No: 2150-7988, IF:0.33] [ISI INDEXED, SCOPUS INDEXED]
3. Joseph Anthony Prathap, T.S.Anandhi, T.S.Sivakumaran "Xilinx Spartan 3A DSP FPGA based DC Voltage Regulators for PV Systems" in the Elsevier Materials Today Proceedings, Volume 5, pp: 1348-1358, February 2018. [ISSN No: 2214-7853, IF:1.24] [ISI INDEXED, SCOPUS INDEXED, Thomson Reuters "Web of Science"].
4. Joseph Anthony Prathap, Maruthi Pottella, Srikanth Thammiseti, Sainath Rachakonda, "FPGA based design of Triple Modular Redundancy for Hybrid Digital Pulse Width Modulation Generator", in the Revista Geintec-Gestao Inovacao E Tecnologias, Vol.11, No.2, pp: 2249-2259, May 2021.[ISSN No: 2237-0722, IF:7.603] [WEB OF SCIENCE, ESCI].
5. Joseph Anthony Prathap, Priyanka Bandaru, Vaishnavi Darshanam, B. Narendar, "Design of Angle based 27-level Trinary Ladder Inverter Using Cross Compiler" in the Revista Geintec-Gestao Inovacao E Tecnologias, Vol.11, No.2, pp: 616-628, April 2021.[ISSN No: 2237-0722, IF:7.603] [WEB OF SCIENCE, ESCI].

Patents Published/Granted: (Latest 2)

1. Filed the patent titled "VLSI Architecture: Advance and High-Performance VLSI Architecture for Montgomery Modular Multiplication Using CMOS VLSI" in Intellectual Property India with Application number 202141049018, published date 27/10/2021.
2. Published the patent titled "IOT Based Optimization Of Solar Power Generation For Efficient Management Of Smart Cities" in Intellectual Property India with Application number 202041046305, published date 06/11/2020.

On-Going Research Projects: NIL